

# Walter Riess

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Dr. Walter Riess joined the IBM Zurich Research Laboratory in 1995. From 1998 to 2003 he was Manager of the Display Technology Group working on Organic Light Emitting Devices for Display applications. From 2004 to 2008 he was managing the Nanoscale Structures and Device group where he was working on ultimate and post CMOS devices.

Since November of 2008, Dr. Riess has been the Department Head of the Science & Technology Department at the IBM Zurich Research Laboratory. Research activities in the department span: chip cooling, advanced thermal packaging, optical interconnects, silicon photonics, nanofabrication, 3D integration, self-assembly and patterning, experimental biology, magnetism, spintronics, AFM & STM imaging, III-V and Ge on silicon, semiconducting nanowires and molecular electronics.

Dr. Walter Riess studied Physics at the University of Bayreuth, Germany, where he received a Masters, a Ph.D and a habilitation.

